

Dr David S. Scott, Technical Director, High Performance Computing, Intel Asia-Pacific.

Biosketch

Dr. David Scott is the Technical Director, High Performance Computing (HPC) for Intel in Asia-Pacific responsible for all technical aspects of Intel's HPC go to market and enabling programs in the region.

He joined Intel in 1985, developing algorithms and math libraries for a sequence of distributed memory message passing computers, culminating in the first teraflop on the Linpack benchmark in 1996. Before moving to his current position in Singapore in 2003, David worked for five years in the Floating Point Center of Expertise, helping to improve floating-point design and validation for all of Intel's 32 and 64-bit processor projects.

David received his PhD in Numerical Analysis from UC Berkeley in 1978. He was a research scientist at the Oak Ridge National Lab for 3 years and taught in the Computer Sciences department of the University of Texas for four years before joining Intel. While working for Intel, he received his MBA from the University of Oregon in 1990.